Amendments to the Specification

Please replace the paragraph [0030] at pages 14-15, with the following rewritten paragraph:

[0030] FIG. 1 is a block diagram showing the structure of a communication device according to a first embodiment of the present ~~embodiment~~ invention;

FIG. 2 is a flowchart showing a transmitting process performed by a transmitter 2;

FIG. 3 is a flowchart showing a receiving process performed by a receiver 3;

FIG. 4 is an illustration for describing the structures of a frame and a packet processed by the transmitter 2;

FIG. 5 is an illustration for describing the structures of a frame and a packet processed by the receiver 3;

FIGS. 6A and 6B are illustrations for describing effects of a communication method according to the first embodiment when a burst error occurs;

FIG. 7 is a block diagram showing the structure of a communication device according to a second embodiment of the present invention;

FIG. 8 is a flowchart showing a transmitting process performed by a transmitter 6;

FIG. 9 is a flowchart showing a receiving process performed by a receiver 7;

FIG. 10 is an illustration for describing the structures of a frame and a packet processed by the transmitter 6;

FIG. 11 is an illustration for describing the structures of a frame and a packet processed by the receiver 7;

FIGS. 12A to 12C are illustrations for describing effects of a communication method according to the second embodiment when a burst error occurs;

FIG. 13 is a sequence diagram corresponding to communications shown in FIGS. 12A to 12C;

FIG. 14 is an illustration for describing a conventional transmission technique using an interleaving method;

FIG. 15 is an illustration for describing that a burst error cyclically occurs in synchronization with commercial power; and

FIG. 16 is a block diagram showing an exemplary structure of a conventional communication device.

Please replace the paragraph [0038] at pages 19-20, with the following rewritten paragraph:

[0038] The frame generating section 22 further calculates an error correcting code with respect to the error detecting frame, and adds the calculated error ~~detecting~~ correcting code to the error detecting frame (step S202). In this example, a case in which the Golay (24, 12) code, which has been described in the BACKGROUND OF THE INVENTION section, is used as an error correcting code. In a case where the Golay (24, 12) code is used, an error correcting code of 12 bits is sequentially calculated and added for every 12 bits from a head of the error detecting frame of 15 bytes (120 bits) for which error correction is to be performed. Thus, as shown in part (c) of FIG. 4, the error correcting frame generated by adding the error correcting code includes ten error correcting blocks, and a frame length L thereof is 30 bytes.

Please replace the paragraph [0039] at pages 20-21, with the following rewritten paragraph:

[0039] Here, a cycle of a burst error occuring over the transmission medium 4 (in the course of transmission) is predicted. An exemplary case in which the communication device of the present invention is applied to power line carrier communications and the burst error occurs due to noise synchronized with 50 Hz commercial power will be described herein. In this case, a cycle T of the burst error is 10ms (=1s/50Hz/2), which is a half-wavelength cycle of the commercial power. Also, the above 10ms is 12 bytes (=~~10~~0.01×9600=96 bits) if it is converted into the number of

bytes in consideration of communication speed of 9600 bits/second, which is practically used in the power line carrier communications. As a result, in the above case, it is predicted that the burst error occurs for every data of 12 bytes.